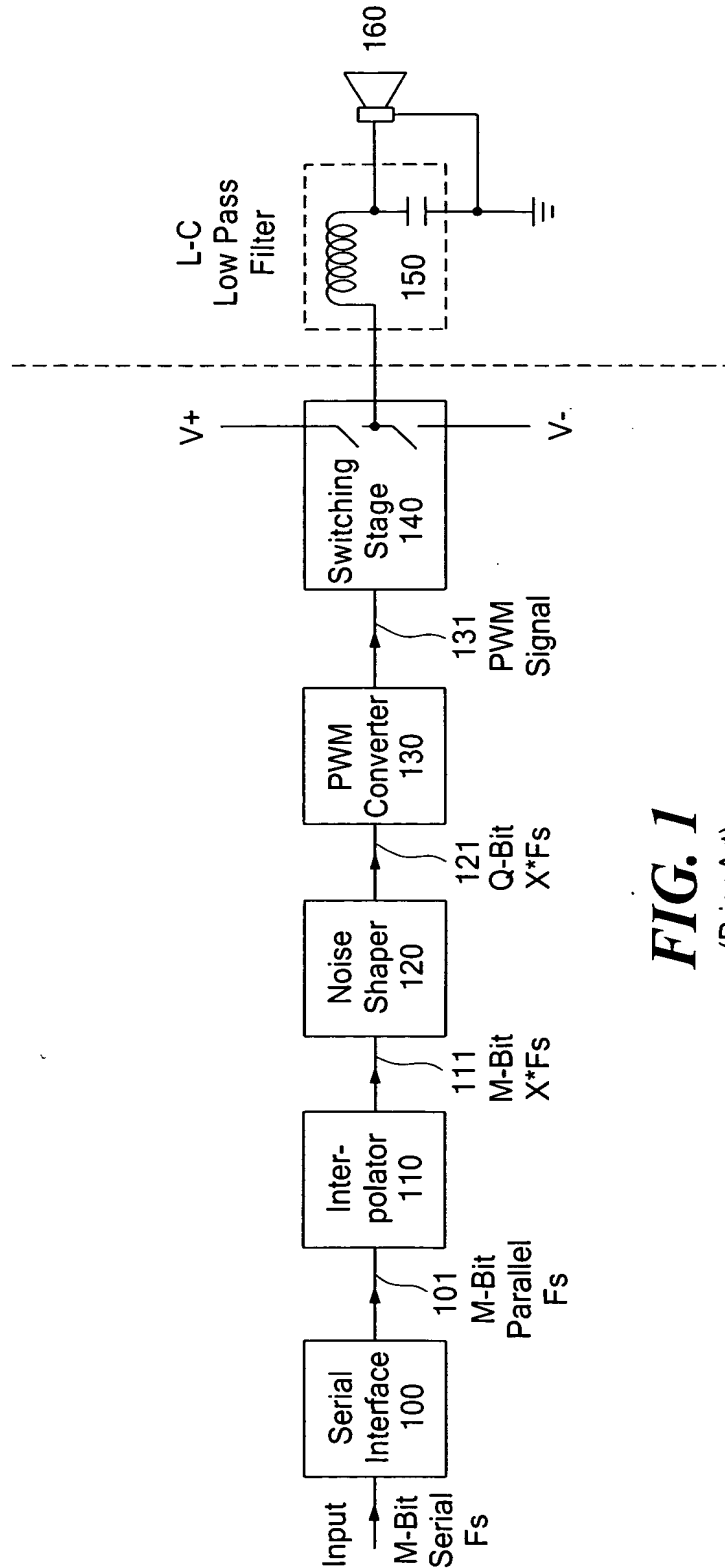




Title: MULTI-LEVEL PULSE WIDTH  
MODULATION IN DIGITAL SYSTEM  
Inventor Name: Ying Lau Lee  
Appl. No. 10/678,614  
Docket No.: HLEE-001XX

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**FIG. 1**  
(Prior Art)

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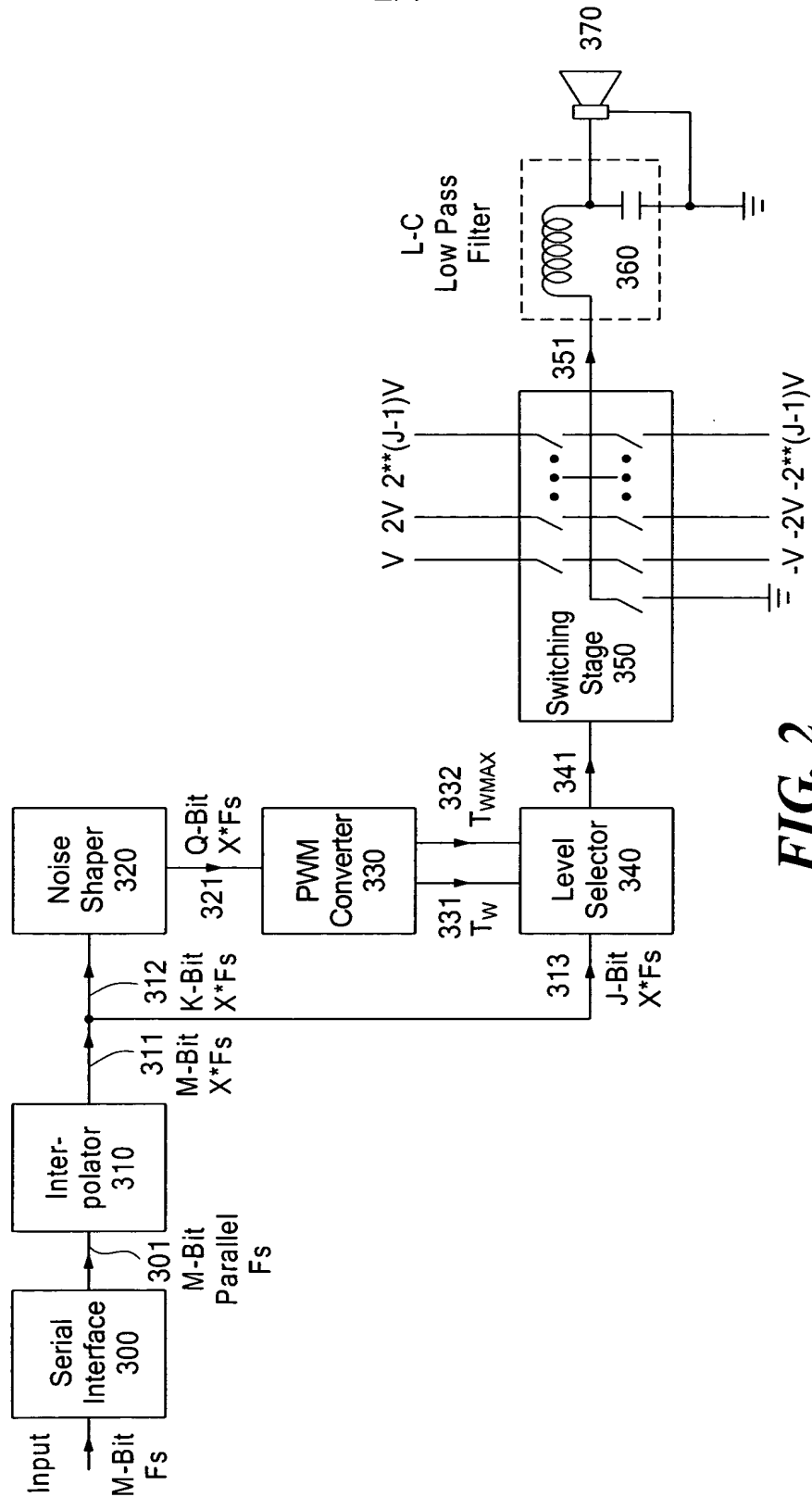
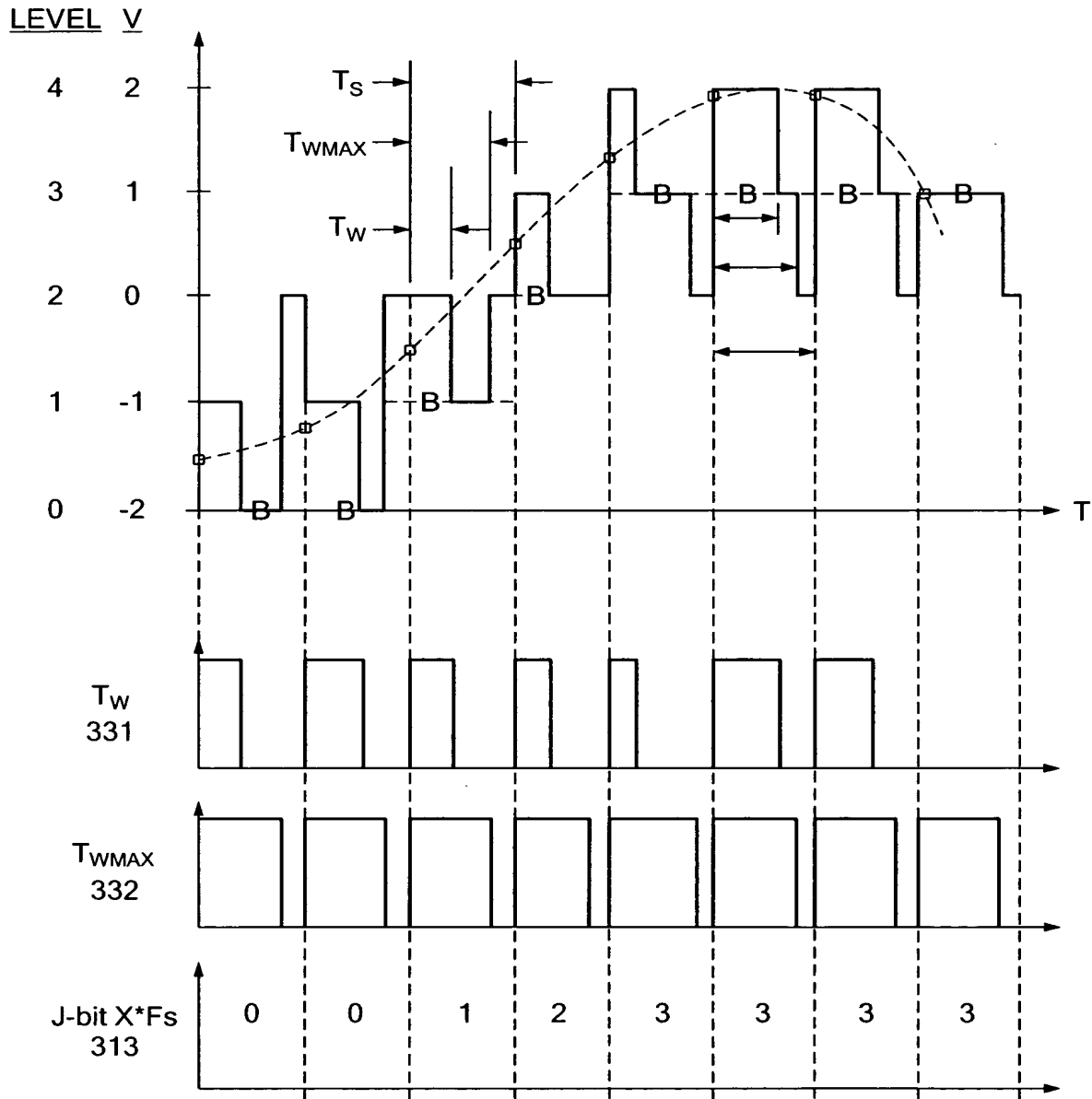


FIG. 2

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-e- Input Data Sample  
 $T_s$  Sampling Cycle-Time  
 $T_w$  PWM Pulse Width  
 $T_{wMAX}$  PWM Pulse Maximum Width  
-B- Base Level



**FIG. 3**

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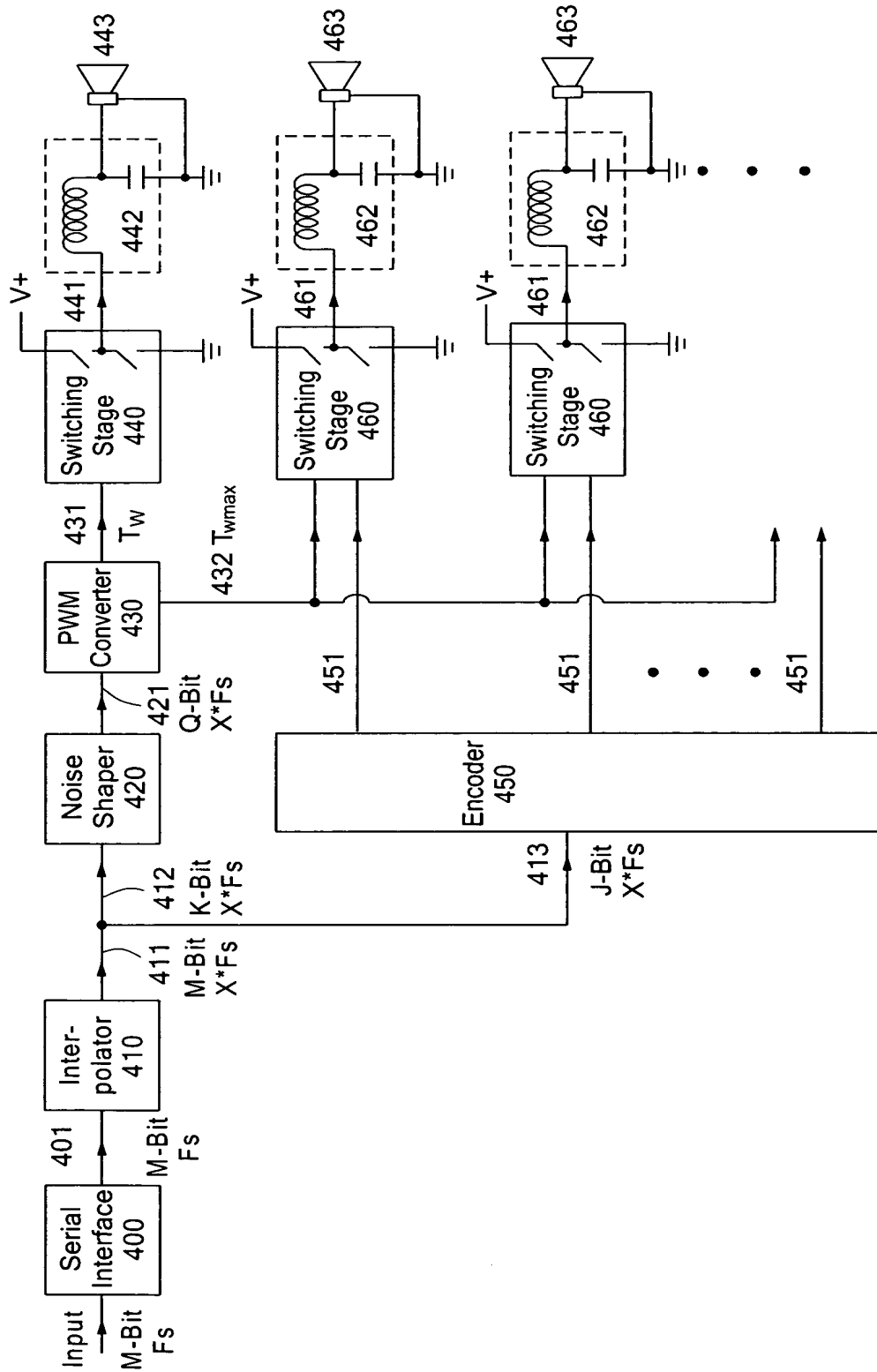


FIG. 4

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5a  
5b

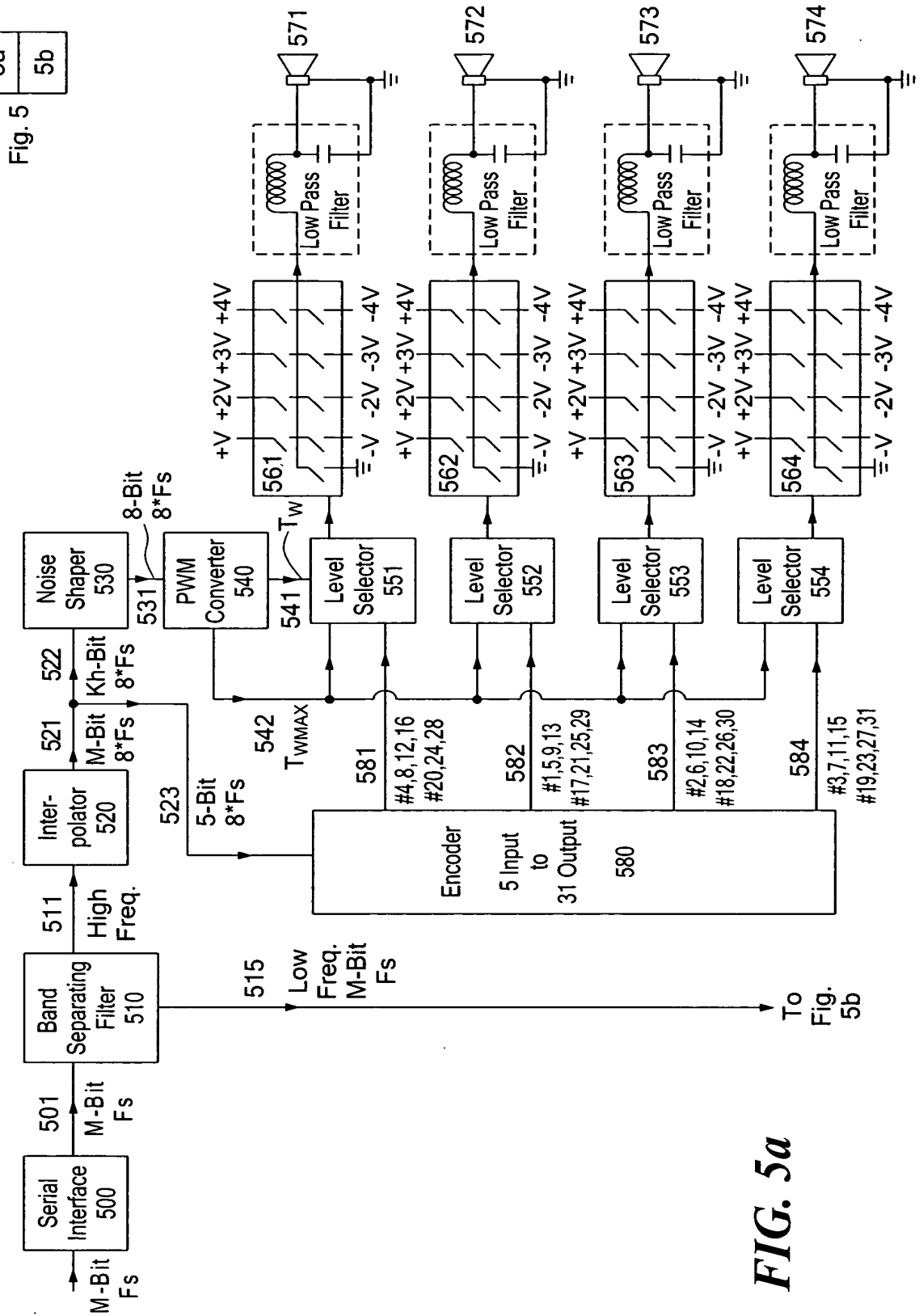
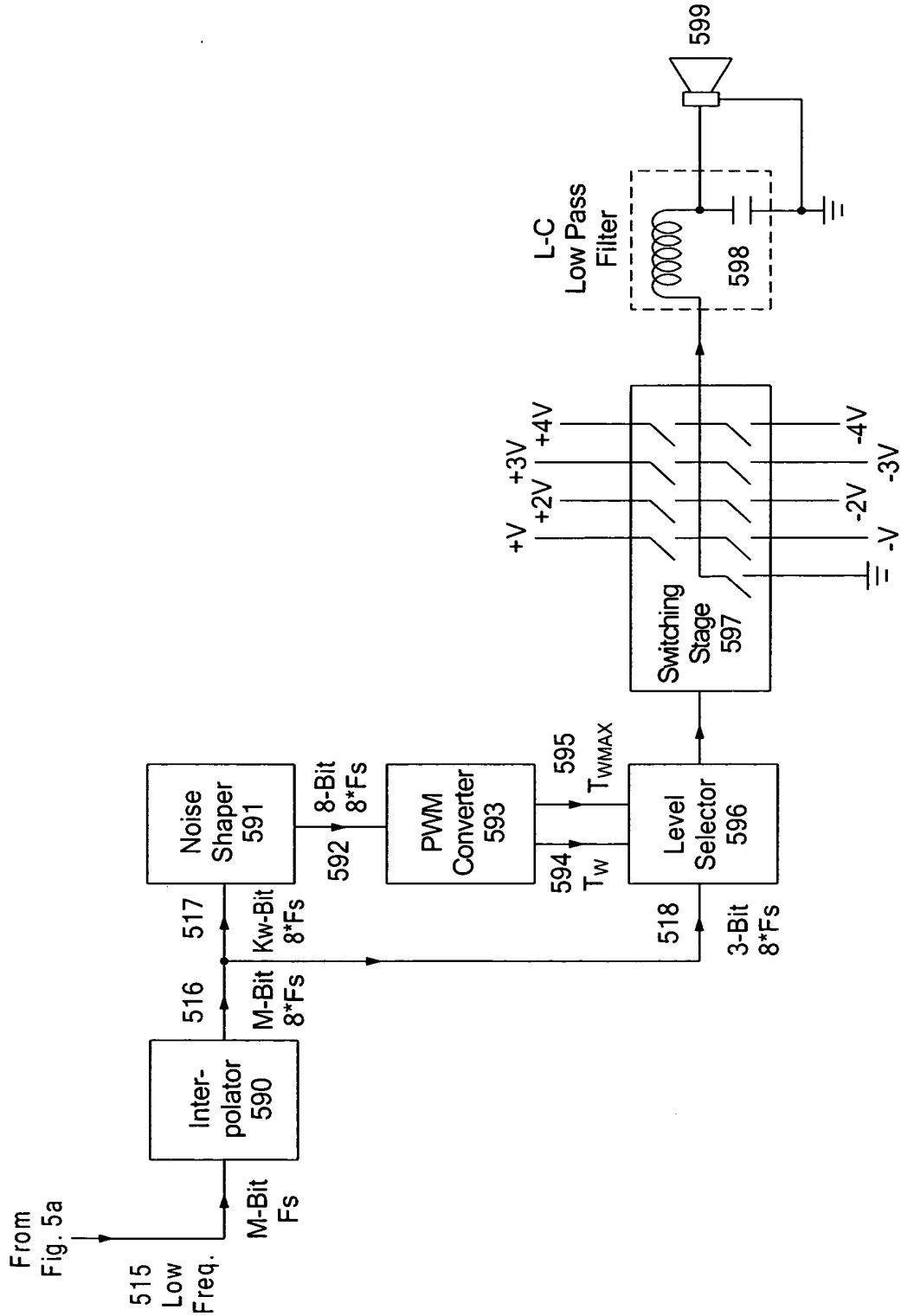


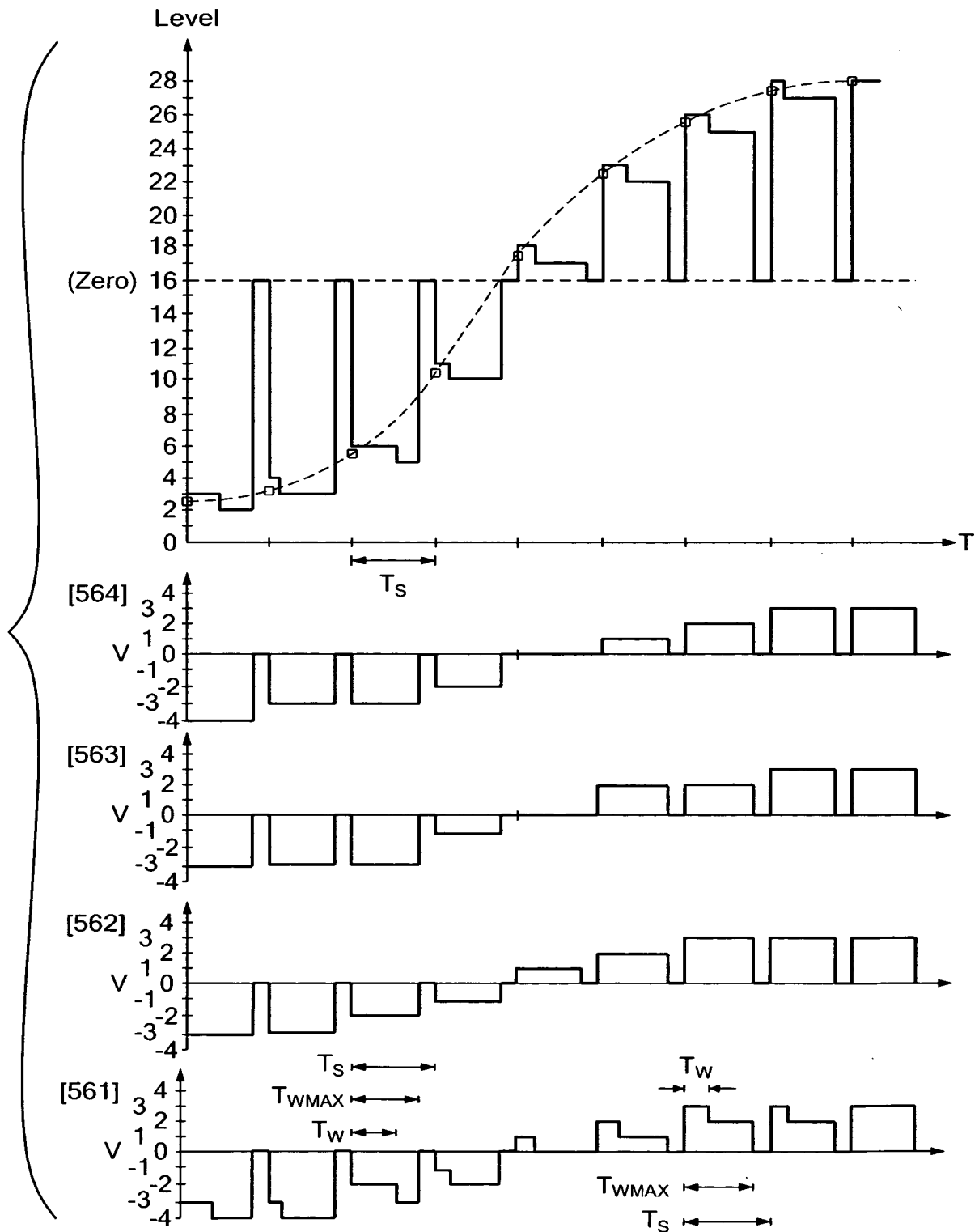
FIG. 5a

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**FIG. 5b**

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**FIG. 6**